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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10079336	FILING DATE 02/19/2002	CLASS 260	SUBCLASS 156.111	GAU 3618	EXAMINER Boris Benenson
<b>**APPLICANTS:</b> Vashchenko Vladislav; Concannon Ann; Hopper Peter; 2406					
<b>**CONTINUING DATA VERIFIED:</b> none					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> FRANCE FR/01.02413 02/22/2001 does not correspond to the application					
PG-PUB DO NOT PUBLISH <input checked="" type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO P05143	
Verified and Acknowledged Examiners's initials					
TITLE : ESD protection methods and devices using additional terminal in the diode structures U.S. DEPT. OF COMM/PAT. & TM-PTO-435L (Rev. 12-94)					

<b>NOTICE OF ALLOWANCE MAILED</b>		Assistant Examiner	<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		Primary Examiner	<b>DRAWING</b>	
Amount Due	Date Paid		Sheets Drawg.	Figs. Drawg.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	Application Examiner	
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